Appl. No. 09/439,061   <u>PATENT</u>
Amdt. dated August 7, 2003
Amendment under 37 CFR 1.116 Expedited Procedure
Examining Group

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1   1-19. (Canceled).

1   20. (Previously presented) In a video data processing apparatus including a controller
2 coupled to a memory circuit that has a plurality of arrays, a method for storing video data in the
3 memory circuit comprising the steps of:
4   segmenting a plurality of pixels representing one horizontal line of an image display
5 panel into a plurality of pixel groups; and
6   storing data representing each of said plurality of pixel groups, respectively, in a row of a
7 plurality of non-adjoining arrays in the memory circuit.

1   21. (Previously presented) The method of claim 20 further comprising the steps of:
2   defining tiles of pixels by a two-dimensional grouping of the plurality of pixel groups
3 from a plurality of horizontal lines of the display panel; and
4   storing data representing each tile of pixels substantially entirely in non-adjoining arrays
5 in the memory circuit.

1   22. (Previously presented) The method of claim 21 wherein the data representing each of
2 said plurality of pixel groups is respectively stored in the same row of the plurality of non-
3 adjoining arrays.

1   23. (Previously presented) The method of claim 22 wherein the data representing each
2 tile of pixels is respectively stored in consecutive rows of the plurality of non-adjoining arrays.

1   24. (Previously presented) The method of claim 23 further comprising the steps of:
2   dividing the display panel into a first half and a second half;
3   storing pixel data from the first half of the panel in odd numbered arrays in the memory
4 circuit; and.

5   storing pixel data from the second half of the panel in even numbered arrays in the

6   memory circuit.

1   25. (Previously presented) In a video data processing apparatus including a controller

2   coupled to a memory circuit that has a plurality of arrays, a method for storing video data in the

3   memory circuit comprising the steps of:

4   dividing the display panel into a first half and a second half;

5   storing pixel data from the first half of the panel in odd numbered arrays in the memory

6   circuit; and

7   storing pixel data from the second half of the panel in even numbered arrays in the

8   memory circuit.